



---

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

---

In re application of: RAMBALDI et al.

Atty Dkt No.: SGSTP009D1/97-B-068D1

Application No.: 10/613,830

Examiner: Hannett, J.

Filed: July 3, 2003

Group: 2622

Title: PIXEL CORRECTION SYSTEM AND  
METHOD FOR CMOS IMAGERS

Confirmation No: 6799

---

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as first-class mail on October 12, 2007 in an envelope addressed to the Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450.

Signed: \_\_\_\_\_

Danielle Jones

**PRELIMINARY AMENDMENT**

Mail Stop RCE  
Commissioner for Patents  
P.O. Box 1450  
Arlington, VA 22313-1450

Please consider the following amendments and remarks filed concurrently with the Request for Continued Examination (RCE).

**Amendments to the Claims** are reflected in the listing of claims which being on page 2 of this paper.

**Remarks** begin on page 5 of this paper.